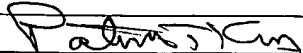


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.: NAN012

Inventor(s): John M. Callahan

Serial No.: 09/642,858

Group Art Unit: Unassigned

Filed: August 21, 2000

Examiner: Unassigned

Title: ROM ERROR CORRECTION CONTROL

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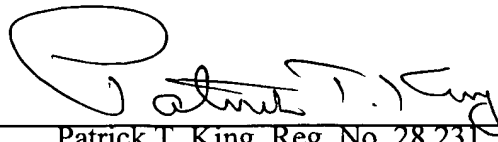
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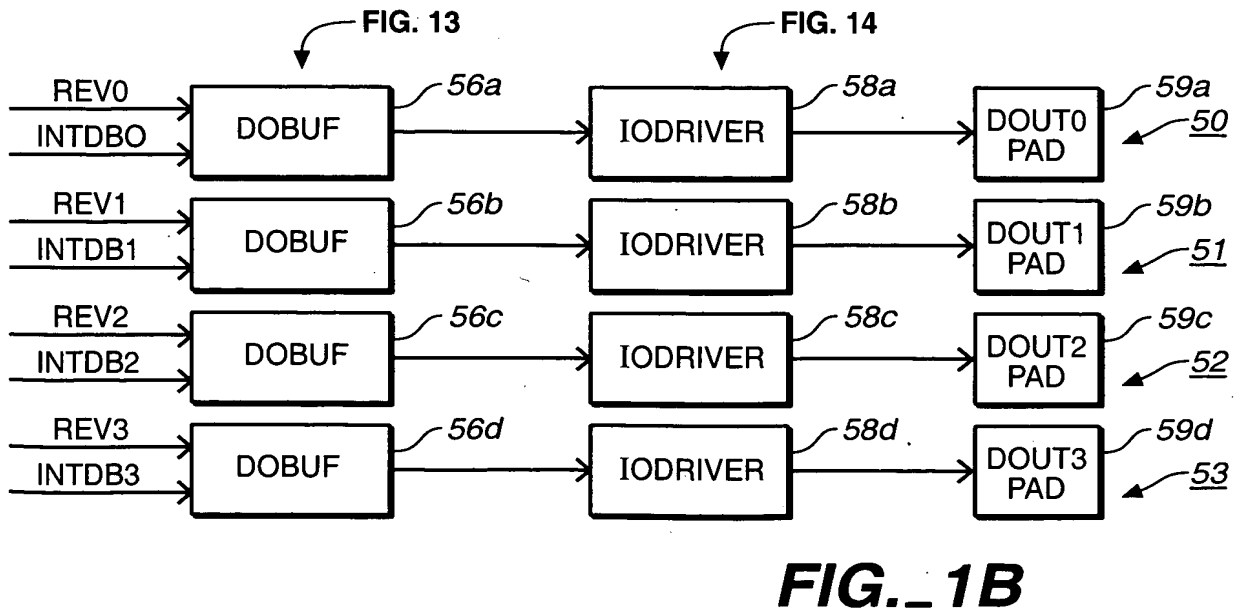
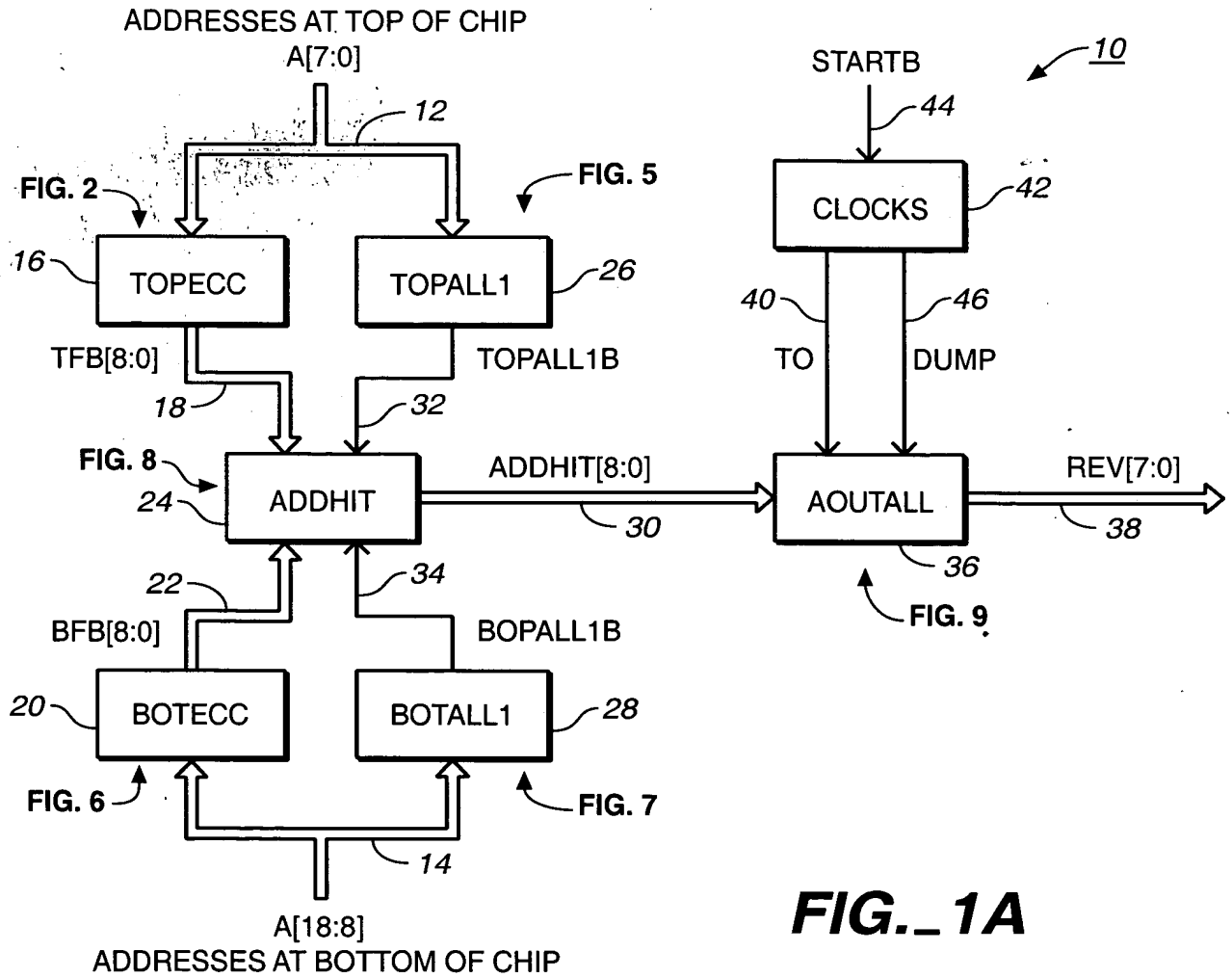
1. Applicant submits herewith, Formal Drawings for this application. Number of sheets of drawings submitted: 12.

Date May 8, 2001

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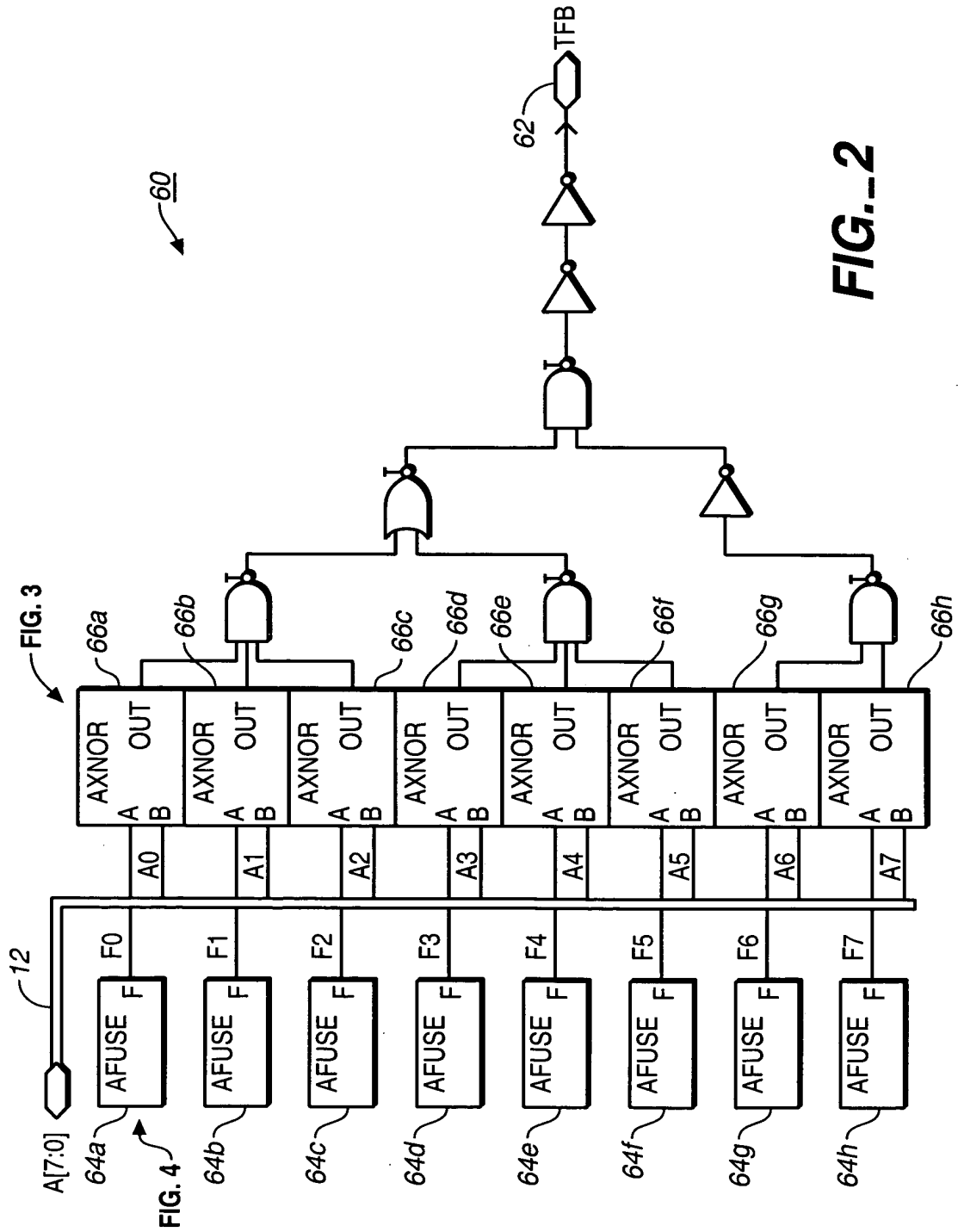


FIG. 2

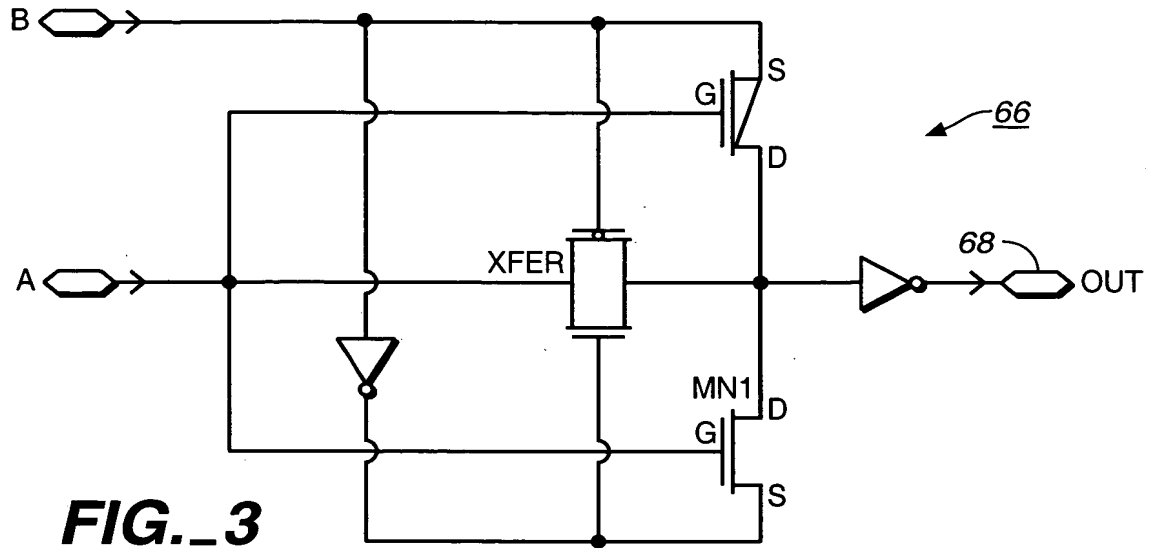


FIG. 3

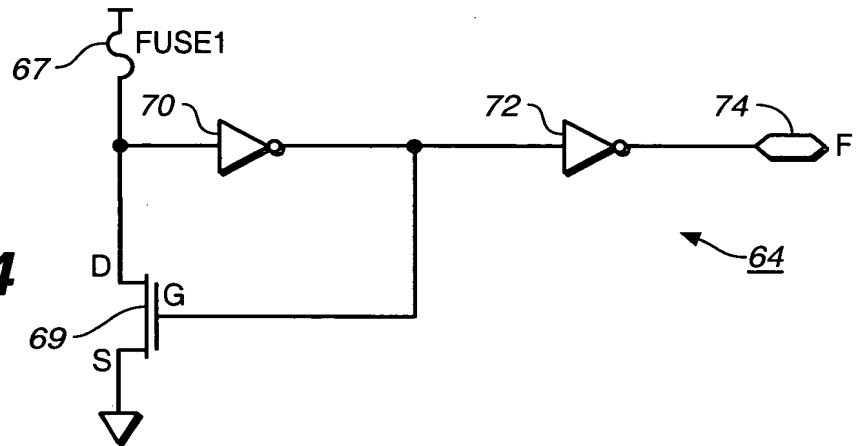


FIG. 4

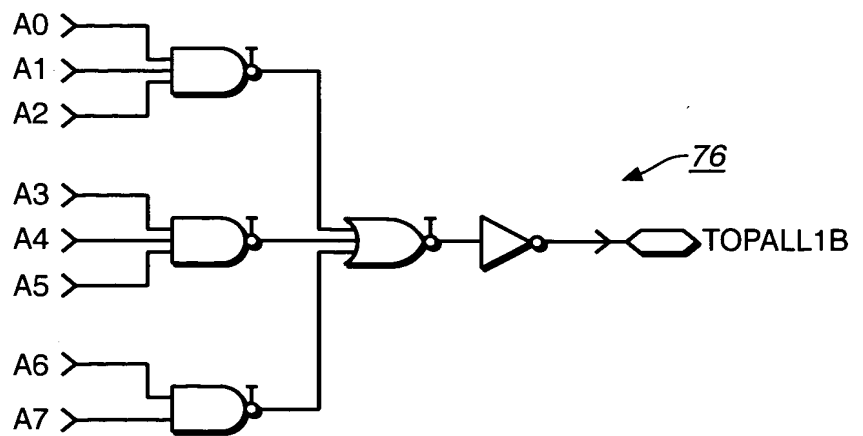


FIG. 5

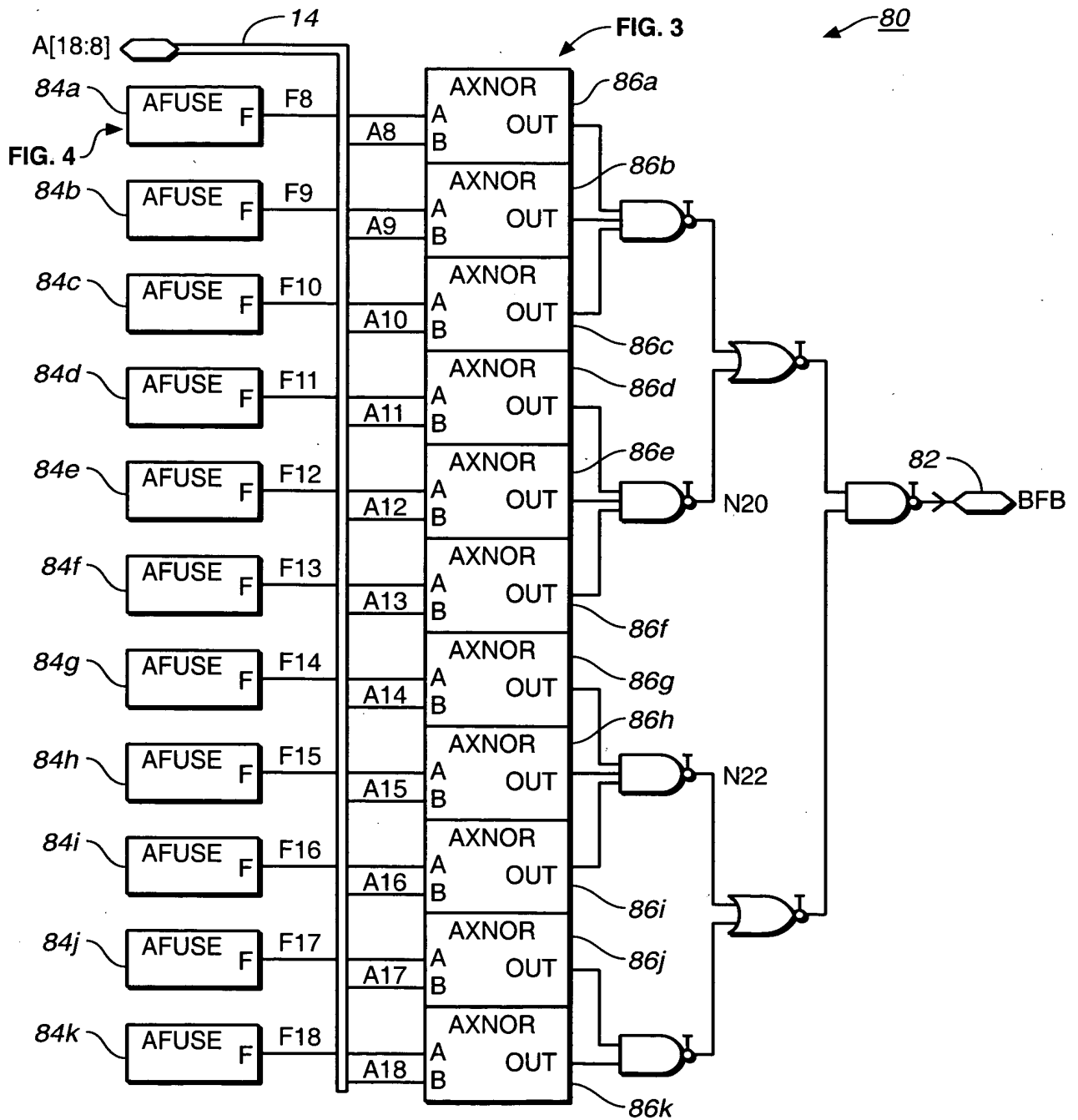


FIG. 6

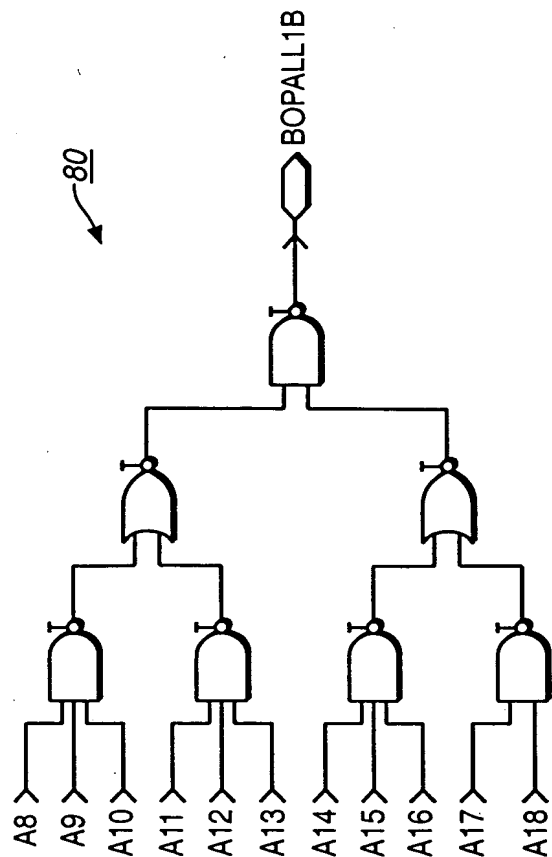


FIG. 7

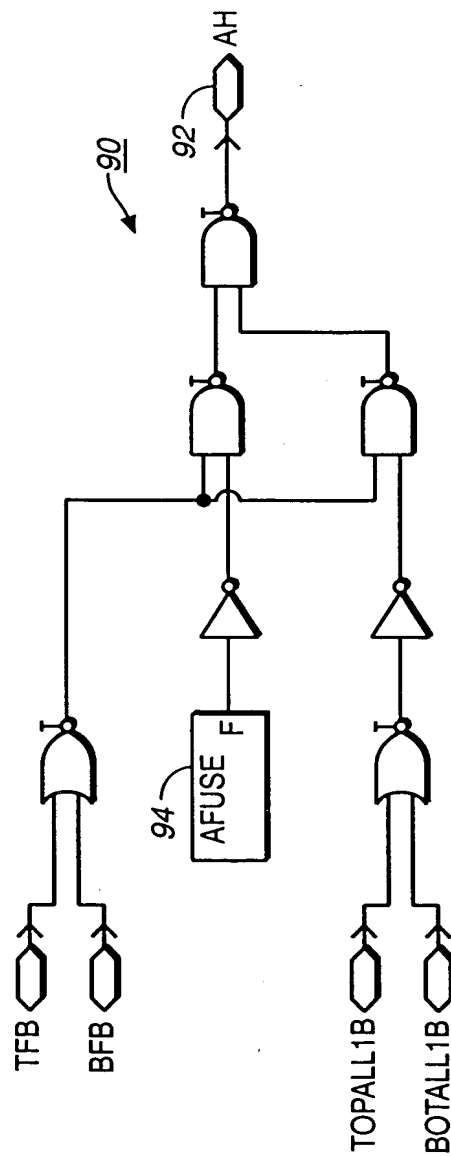


FIG. 8

Fig. 8B

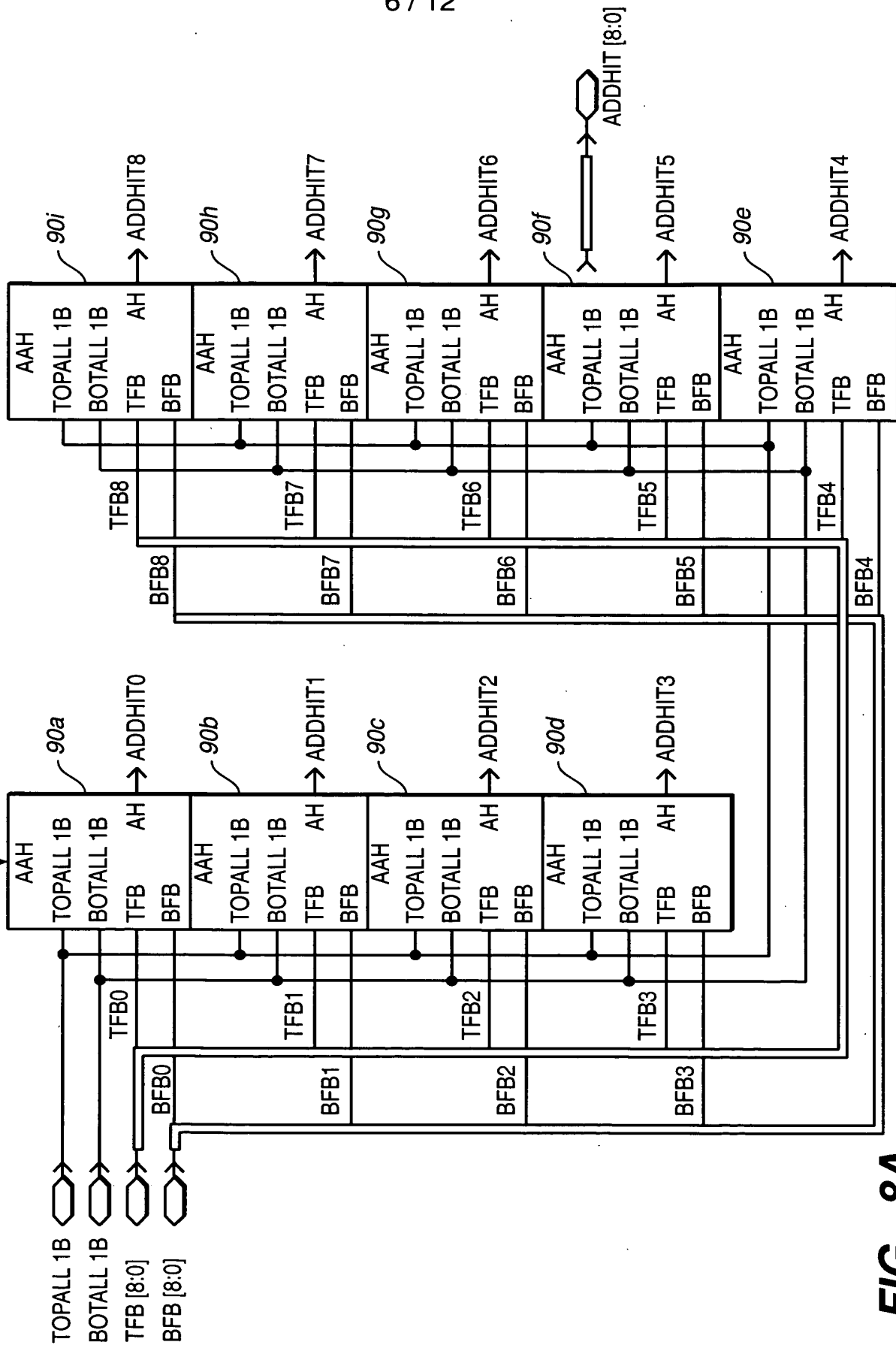


FIG.-8A

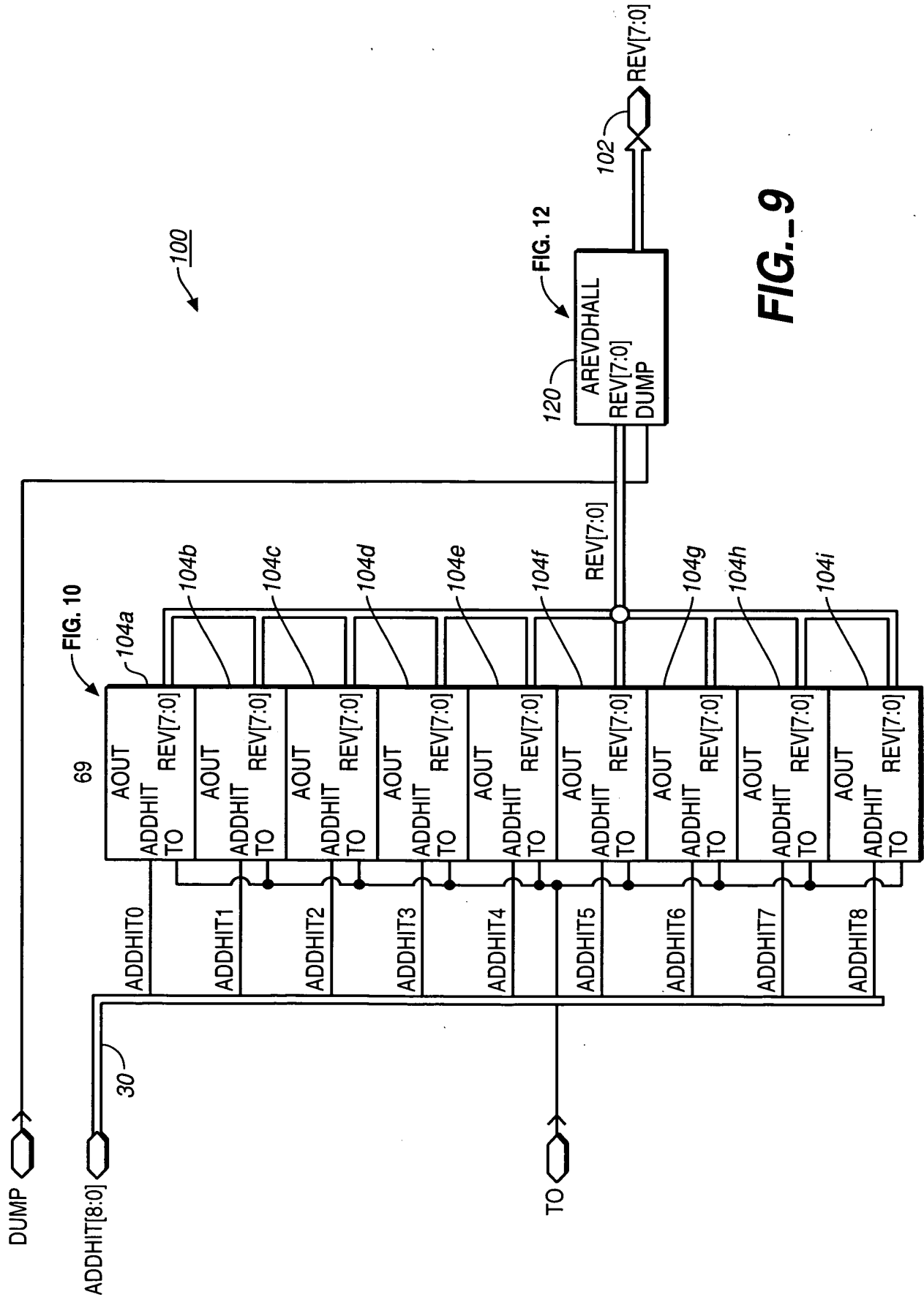


FIG. 9

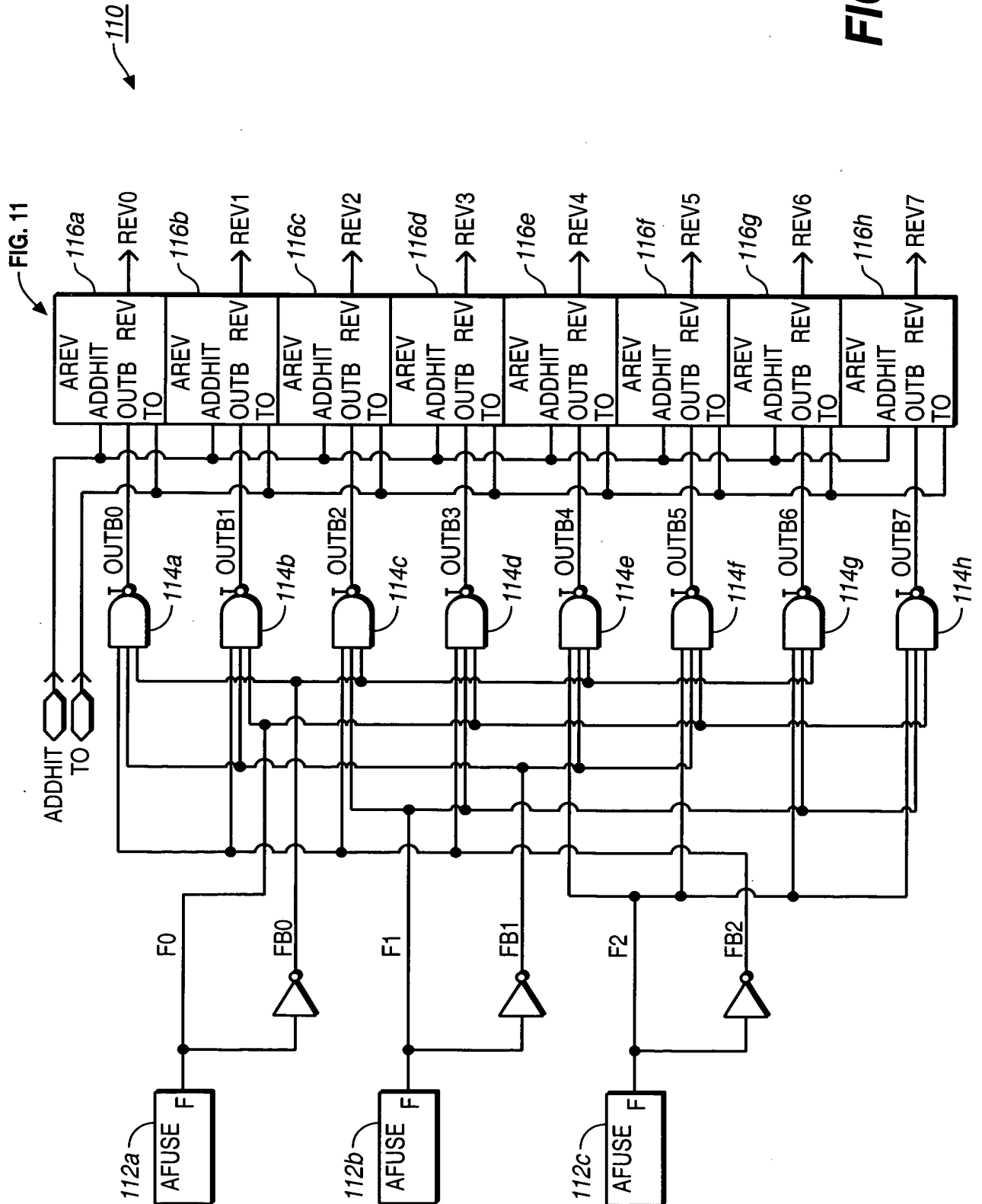
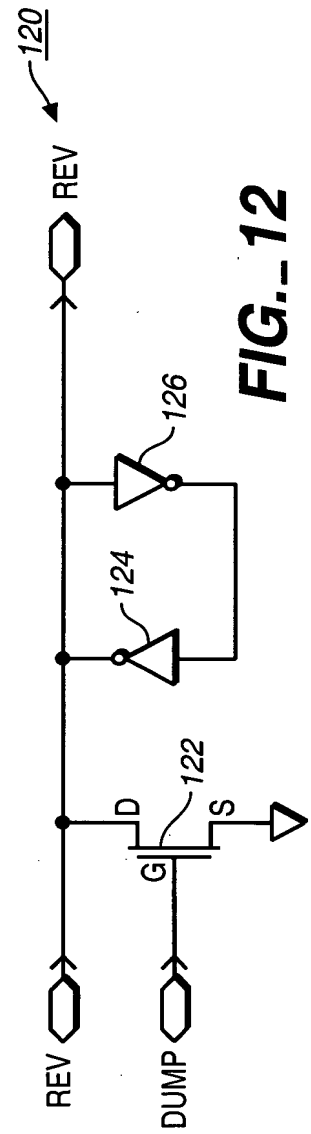
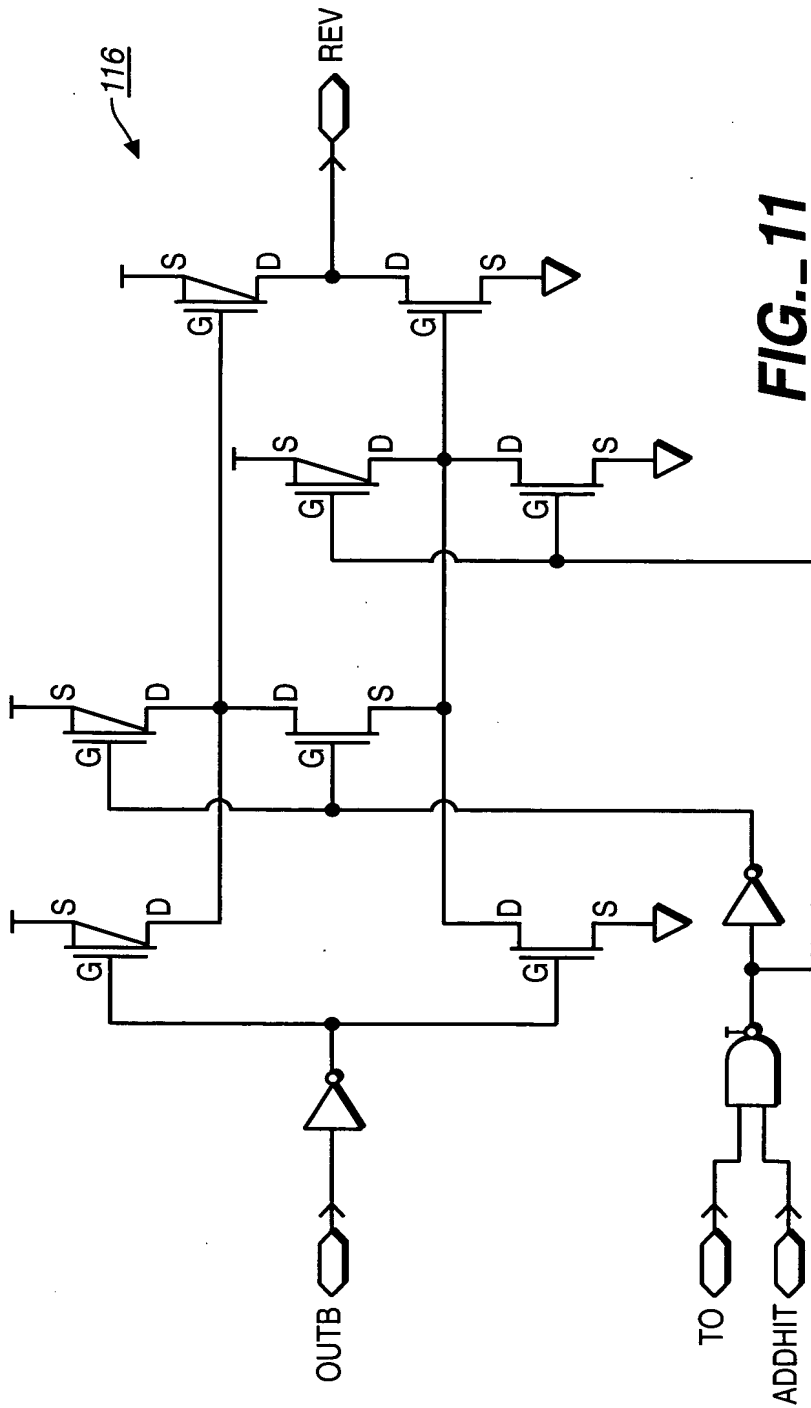
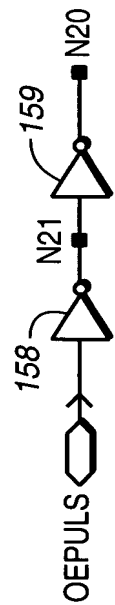
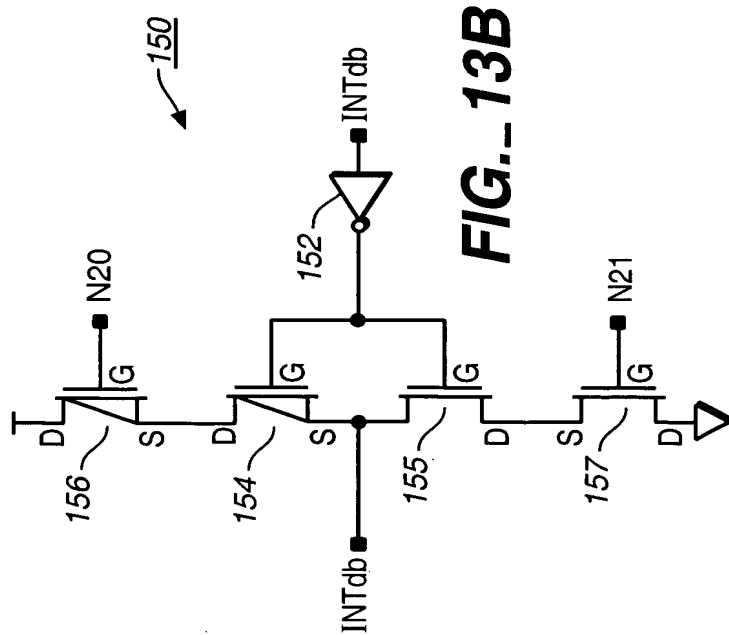
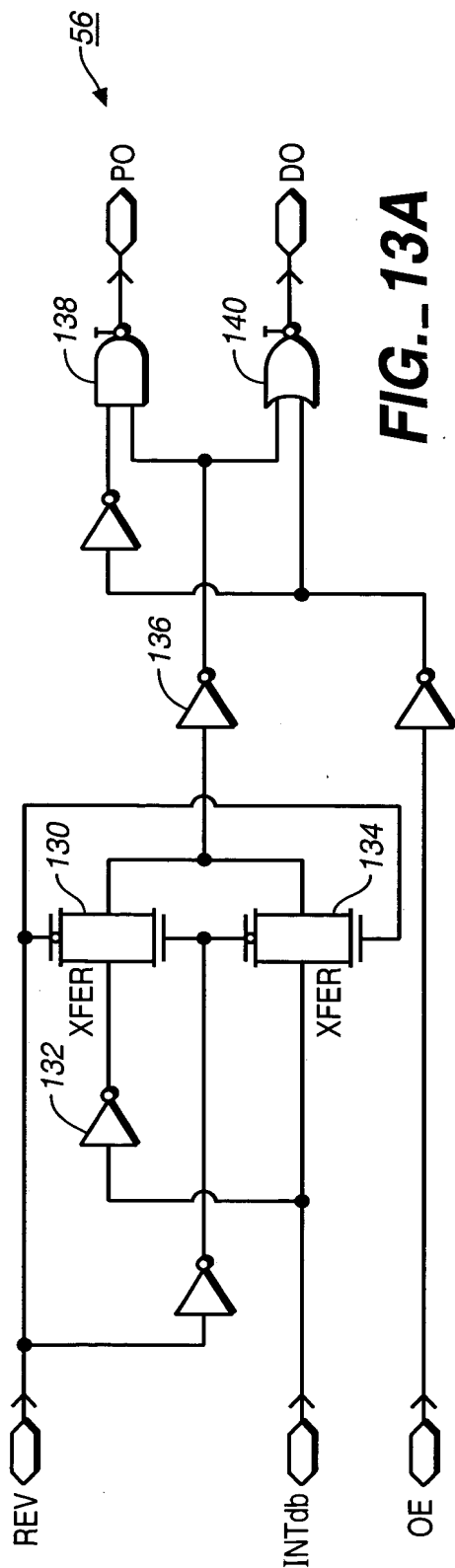


FIG. 10





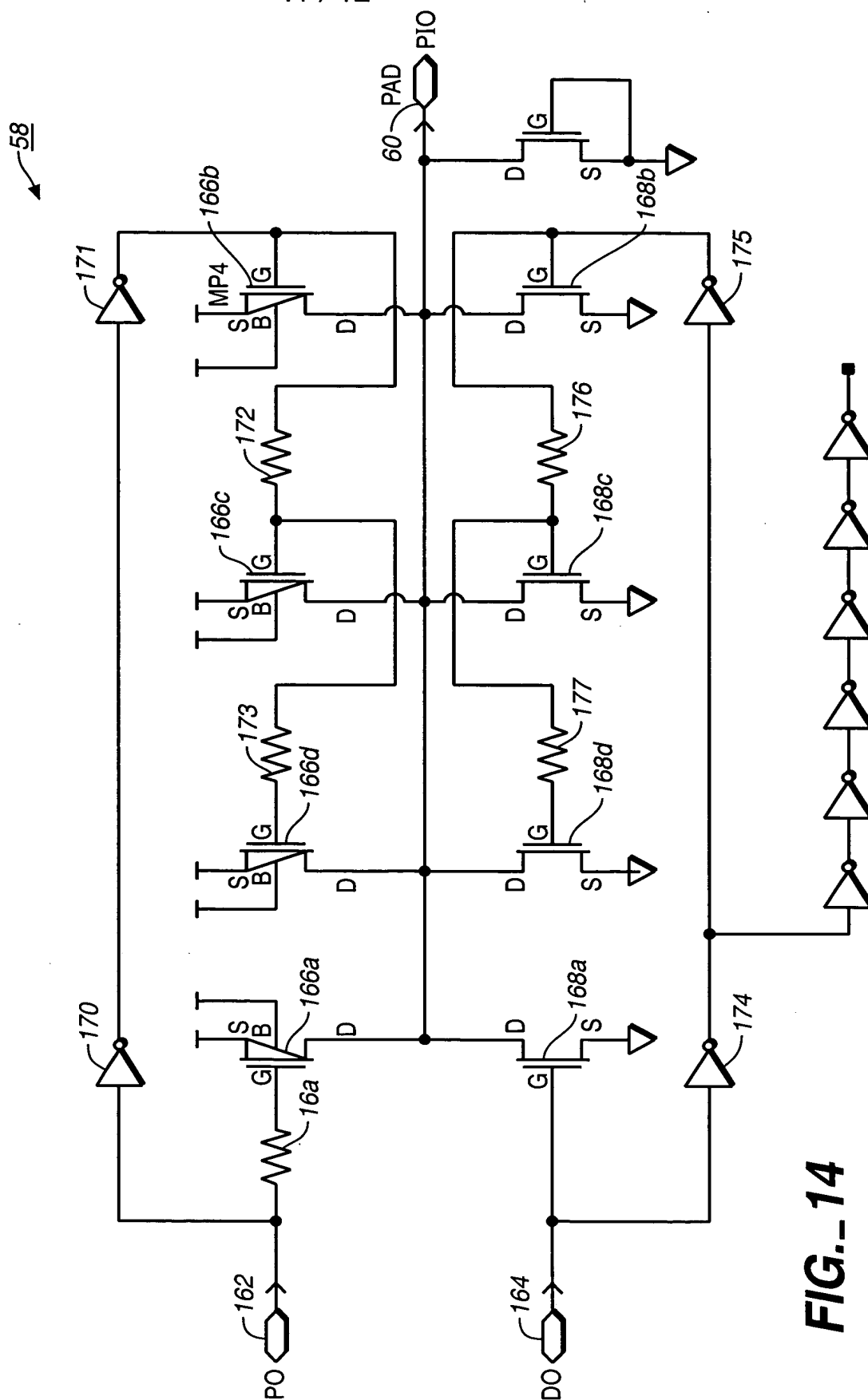


FIG. 14

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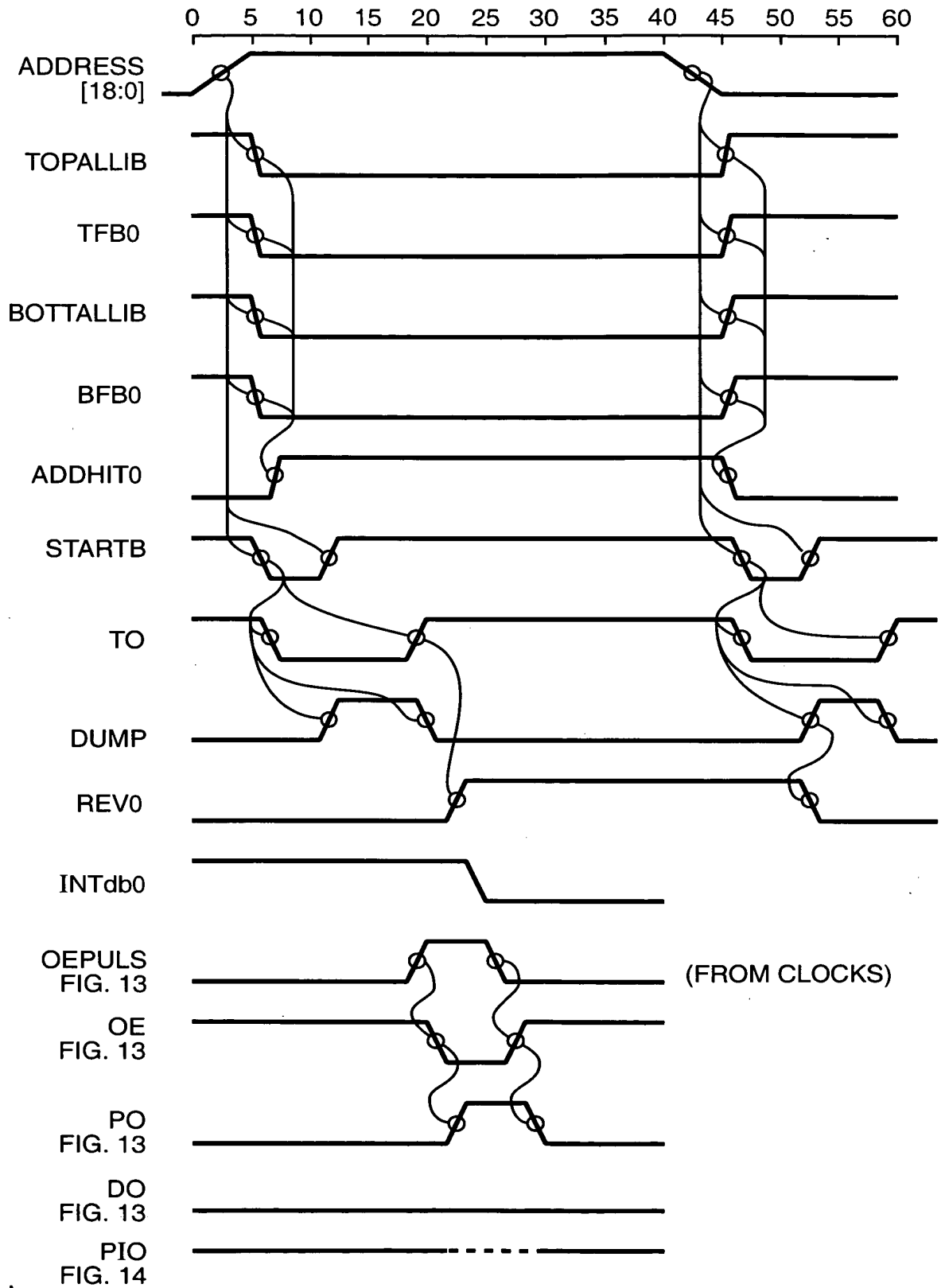


FIG. 15